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COC

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November 30, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Title: METHOD FOR MANUFACTURE OF MAGNETO-RESISTIVE BIT  
STRUCTURE  
Letters Patent No. 6,872,997  
Issued: March 29, 2005  
Our Reference: MICRON.230C1

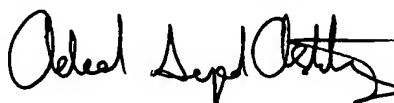
Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP



Adeel S. Akhtar  
Registration No. 41,394  
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Certificate  
DEC 07 2005  
of Correction

Enclosures

2121317  
112905

DEC 7 2005

Orange County  
949-760-0404

San Diego  
619-235-8550

Los Angeles  
310-551-3450

Riverside  
951-781-9231

San Luis Obispo  
805-547-5580

# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

**PATENT NO. :** 6,872,997

**DATED :** March 29, 2005

**INVENTOR(S):** Harry Liu, William Larson, Lonny Berg, Theodore Zhu, Shaoping Li, Romney R. Katti,  
Yong Lu, Anthony Arrott

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 36, delete "at" and insert - -al- - therefor;

Column 6, line 19, delete "end" and insert - -ends- - therefor.

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,872,997

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November 29, 2005

MICRON.230C1  
FORM PTO 1050

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